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JUN 21 2007

PATENT

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2826

Rev 07/05

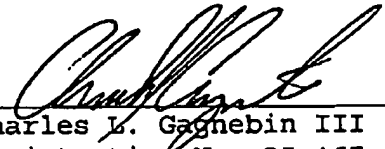
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application : Mark N. Horenstein
Application No. : 10/506,654
Filed : September 3, 2004
Confirmation No. : 4807
For : METHOD FOR LINEARIZING DEFLECTION OF A MEMS
DEVICE USING BINARY ELECTRODES AND VOLTAGE
MODULATION
Examiner : Tan N. Tran
Attorney's Docket : BU-097XX

*enter per
RCE filed
12-607*

TC Art Unit: 2826

I hereby certify that this correspondence is being sent via
facsimile to Examiner Tan N. Tran, TC Art Unit 2826, Fax No. (571)
273 8300, on 6-21-7.

By: 
Charles L. Gagnebin III
Registration No. 25,467
Attorney for Applicant(s)

AMENDMENT

Via Facsimile
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 7, 2007, please
amend the above-identified patent application as follows.

WEINGARTEN, SCHURGIN,
GAGNEBIN & LEDOVICKI LLP
TEL. (617) 542-2390
FAX. (617) 451-0313